









SNIS159G -AUGUST 1999-REVISED AUGUST 2016

LM35

LM35 Precision Centigrade Temperature Sensors

Features

- Calibrated Directly in Celsius (Centigrade)
- Linear + 10-mV/°C Scale Factor
- 0.5°C Ensured Accuracy (at 25°C)
- Rated for Full -55°C to 150°C Range
- Suitable for Remote Applications
- Low-Cost Due to Wafer-Level Trimming
- Operates from 4 V to 30 V
- Less than 60-μA Current Drain
- Low Self-Heating, 0.08°C in Still Air
- Non-Linearity Only ±\(^1\)c Typical
- Low-Impedance Output, 0.1 Ω for 1-mA Load

Applications

- **Power Supplies**
- **Battery Management**
- **HVAC**
- **Appliances**

3 Description

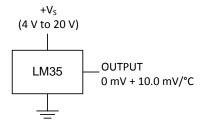
The LM35 series are precision integrated-circuit temperature devices with an output voltage linearlyproportional to the Centigrade temperature. The LM35 device has an advantage over linear temperature sensors calibrated in Kelvin, as the user is not required to subtract a large constant voltage from the output to obtain convenient Centigrade scaling. The LM35 device does not require any external calibration or trimming to provide typical accuracies of ±1/4°C at room temperature and ±3/4°C over a full -55°C to 150°C temperature range. Lower cost is assured by trimming and calibration at the wafer level. The low-output impedance, linear output, and precise inherent calibration of the LM35 device makes interfacing to readout or control circuitry especially easy. The device is used with single power supplies, or with plus and minus supplies. As the LM35 device draws only 60 µA from the supply, it has very low self-heating of less than 0.1°C in still air. The LM35 device is rated to operate over a -55°C to 150°C temperature range, while the LM35C device is rated for a -40°C to 110°C range (-10° with improved accuracy). The LM35-series devices are available packaged in hermetic TO transistor packages, while the LM35C, LM35CA, and LM35D devices are available in the plastic TO-92 transistor package. The LM35D device is available in an 8-lead surface-mount small-outline package and a plastic TO-220 package.

Device Information⁽¹⁾

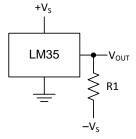
_									
PART NUMBER	PACKAGE	BODY SIZE (NOM)							
	TO-CAN (3)	4.699 mm × 4.699 mm							
LM35	TO-92 (3)	4.30 mm × 4.30 mm							
	SOIC (8)	4.90 mm × 3.91 mm							
	TO-220 (3)	14.986 mm × 10.16 mm							

(1) For all available packages, see the orderable addendum at the end of the datasheet.

Basic Centigrade Temperature Sensor (2°C to 150°C)



Full-Range Centigrade Temperature Sensor



Choose $R_1 = -V_S / 50 \mu A$ $V_{OUT} = 1500 \text{ mV}$ at 150°C $V_{OUT} = 250 \text{ mV}$ at 25°C

 $V_{OUT} = -550 \text{ mV at } -55^{\circ}\text{C}$



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1 2	Features	7.2 Functional Block Diagram
3 4	Description	7.4 Device Functional Modes
5 6	Pin Configuration and Functions 3 Specifications 4 6.1 Absolute Maximum Ratings 4 6.2 ESD Ratings 4 6.3 Recommended Operating Conditions 4 6.4 Thermal Information 4 6.5 Electrical Characteristics: LM35A, LM35CA Limits 5 6.6 Electrical Characteristics: LM35A, LM35CA 6 6.7 Electrical Characteristics: LM35, LM35C, LM35D Limits 8 6.8 Electrical Characteristics: LM35, LM35C, LM35D 9	8.1 Application Information
Try		
	•	Page

Changes from Revision E (January 2015) to Revision F

Page

Changes from Revision D (October 2013) to Revision E

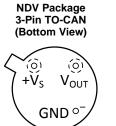
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Changes from Revision C (July 2013) to Revision D

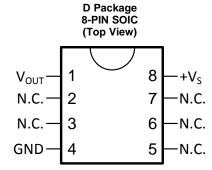
Page



5 Pin Configuration and Functions

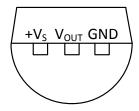


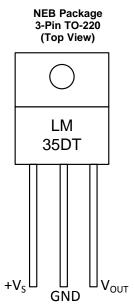
Case is connected to negative pin (GND)



N.C. = No connection

LP Package 3-Pin TO-92 (Bottom View)





Tab is connected to the negative pin (GND).

NOTE: The LM35DT pinout is different than the discontinued LM35DP

Pin Functions

		PIN		TVDE	DESCRIPTION	
NAME	TO46	TO92	TO220	SO8	ITPE	DESCRIPTION
V_{OUT}	_	l	_	1	0	Temperature Sensor Analog Output
N.C.	_	-	_	2		No Connection
N.C.	_	-	_	3	_	No Connection
GND	_	_	_	4	GROUND	Device ground pin, connect to power supply negative terminal
	_		_	5		
N.C.	_		_	6	_	No Connection
	_	-	_	7		
+V _S	_	_	_	8	POWER	Positive power supply pin

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6 Specifications

6.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted) (1)(2)

		MIN	MAX	UNIT	
Supply voltage		-0.2	35	V	
Output voltage		-1	6	V	
Output current			10	mA	
Maximum Junction Temperature, T _J m	ax		150	°C	
	TO-CAN, TO-92 Package	-60	150	°C	
Storage Temperature, T _{stg}	TO-220, SOIC Package	-65	150	°C	

⁽¹⁾ If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/ Distributors for availability and specifications.

6.2 ESD Ratings

			VALUE	UNIT
V _(ESD)	Electrostatic discharge	Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 (1)	±2500	V

⁽¹⁾ JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

		MIN	MAX	UNIT
Specified operating temperature: T _{MIN} to T _{MAX} LM35, LM35A LM35C, LM35CA LM35D	LM35, LM35A	- 55	150	
	LM35C, LM35CA	-40	110	°C
	LM35D	0	100	
Supply Voltage (+V _S)		4	30	V

6.4 Thermal Information

THERMAL METRIC ⁽¹⁾⁽²⁾	NDV	LP	D	NEB	UNIT
	3 P	INS	8 PINS	3 PINS	
$R_{\theta JA}$ Junction-to-ambient thermal resistance	400	180	220	90	°C/W
R _{θJC(top)} Junction-to-case (top) thermal resistance	24	_	_	_	C/VV

⁽¹⁾ For more information about traditional and new thermal metrics, see the IC Package Thermal Metrics application report, SPRA953.

⁽²⁾ Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. DC and AC electrical specifications do not apply when operating the device beyond its rated operating conditions.

⁽²⁾ For additional thermal resistance information, see *Typical Application*.



6.5 Electrical Characteristics: LM35A, LM35CA Limits

Unless otherwise noted, these specifications apply: $-55^{\circ}\text{C} \le \text{T}_{\text{J}} \le 150^{\circ}\text{C}$ for the LM35 and LM35A; $-40^{\circ}\text{C} \le \text{T}_{\text{J}} \le 110^{\circ}\text{C}$ for the LM35C and LM35CA; and $0^{\circ}\text{C} \le \text{T}_{\text{J}} \le 100^{\circ}\text{C}$ for the LM35D. $V_S = 5$ Vdc and $I_{\text{LOAD}} = 50~\mu\text{A}$, in the circuit of Full-Range Centigrade Temperature Sensor. These specifications also apply from 2°C to T_{MAX} in the circuit of Figure 14.

Accuracy ⁽³⁾	TEST CONDITIONS $\Gamma_{A} = 25^{\circ}\text{C}$ $\Gamma_{A} = -10^{\circ}\text{C}$	TYP ±0.2	TESTED LIMIT ⁽¹⁾	DESIGN LIMIT ⁽²⁾	TYP	TESTED	DESIGN	UNIT	
Accuracy ⁽³⁾	Γ _A = -10°C	±0.2	0.5			LIMIT ⁽¹⁾	LIMIT ⁽²⁾		
Accuracy (3)			±0.5		±0.2	±0.5			
T		±0.3			±0.3		±1	°C	
т	$\Gamma_{A} = T_{MAX}$	±0.4	±1		±0.4	±1		C	
	$\Gamma_{A} = T_{MIN}$	±0.4	±1		±0.4		±1.5		
Nonlinearity ⁽⁴⁾	$T_{MIN} \le T_A \le T_{MAX}$, -40°C ≤ $T_J \le 125$ °C	±0.18		±0.35	±0.15		±0.3	°C	
Sensor gain T	$T_{MIN} \le T_A \le T_{MAX}$	10	9.9		10		9.9	mV/°C	
(average slope)	-40°C ≤ T _J ≤ 125°C	10	10.1		10		10.1	IIIV/ C	
Load regulation (5)	Γ _A = 25°C	±0.4	±1		±0.4	±1		mV/mA	
0 ≤ l₁ ≤ 1 mA	$T_{MIN} \le T_A \le T_{MAX}$, -40°C ≤ $T_J \le 125$ °C	±0.5		±3	±0.5		±3		
	Γ _A = 25°C	±0.01	±0.05		±0.01	±0.05			
	¹ V ≤ V _S ≤ 30 V, -40°C ≤ T _J ≤ 125°C	±0.02		±0.1	±0.02		±0.1	mV/V	
V	/ _S = 5 V, 25°C	56	67		56	67			
Quiescent current ⁽⁶⁾	$I_{S} = 5 \text{ V}, -40^{\circ}\text{C} \le T_{J} \le 125^{\circ}\text{C}$	105		131	91		114		
Vulescent current.	/ _S = 30 V, 25°C	56.2	68		56.2	68		μA	
V	$V_{\rm S} = 30 \text{ V}, -40^{\circ}\text{C} \le T_{\rm J} \le 125^{\circ}\text{C}$	105.5		133	91.5		116		
Change of guiescent 4	4 V ≤ V _S ≤ 30 V, 25°C	0.2	1		0.2	1			
current ⁽⁵⁾	¹ V ≤ V _S ≤ 30 V, -40°C ≤ T _J ≤ 125°C	0.5		2	0.5		2	μΑ	
Temperature coefficient of quiescent current	-40°C ≤ T _J ≤ 125°C	0.39		0.5	0.39		0.5	μΑ/°C	
Minimum temperature for rate accuracy	n circuit of Figure 14, I _L = 0	1.5		2	1.5		2	°C	
Long term stability T	$T_{\rm J} = T_{\rm MAX}$, for 1000 hours	±0.08			±0.08			°C	

- (1) Tested Limits are ensured and 100% tested in production.
- (2) Design Limits are ensured (but not 100% production tested) over the indicated temperature and supply voltage ranges. These limits are not used to calculate outgoing quality levels.
- (3) Accuracy is defined as the error between the output voltage and 10 mv/°C times the case temperature of the device, at specified conditions of voltage, current, and temperature (expressed in °C).
- (4) Non-linearity is defined as the deviation of the output-voltage-versus-temperature curve from the best-fit straight line, over the rated temperature range of the device.
- (5) Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output due to heating effects can be computed by multiplying the internal dissipation by the thermal resistance.
- (6) Quiescent current is defined in the circuit of Figure 14.



6.6 Electrical Characteristics: LM35A, LM35CA

Unless otherwise noted, these specifications apply: $-55^{\circ}\text{C} \le T_{J} \le 150^{\circ}\text{C}$ for the LM35 and LM35A; $-40^{\circ}\text{C} \le T_{J} \le 110^{\circ}\text{C}$ for the LM35C and LM35CA; and $0^{\circ}\text{C} \le T_{J} \le 100^{\circ}\text{C}$ for the LM35D. $V_{S} = 5$ Vdc and $I_{LOAD} = 50$ μA , in the circuit of Full-Range Centigrade Temperature Sensor. These specifications also apply from 2°C to T_{MAX} in the circuit of Figure 14.

			LM35A			LM35CA			
PARAMETER	TEST CO	NDITIONS	MIN	TYP	MAX	TYP	TYP	MAX	UNIT
				±0.2			±0.2		
	T _A = 25°C	Tested Limit (2)			±0.5			±0.5	
		Design Limit ⁽³⁾							
				±0.3			±0.3		
	T _A = -10°C	Tested Limit (2)							
. (1)		Design Limit ⁽³⁾						±1	
Accuracy ⁽¹⁾				±0.4			±0.4		°C
	$T_A = T_{MAX}$	Tested Limit (2)			±1			±1	
	7. 1000	Design Limit ⁽³⁾							
				±0.4			±0.4		
	$T_A = T_{MIN}$	Tested Limit (2)			±1				
	· A · MIIN	Design Limit ⁽³⁾						±1.5	
				±0.18			±0.15		
Nonlinearity (4)	$T_{MIN} \le T_A \le T_{MAX}$	Tested Limit (2)							°C
. toriiirioarity	-40°C ≤ T _J ≤ 125°C	Design Limit ⁽³⁾			±0.35			±0.3	
				10			10		
	$T_{MIN} \le T_A \le T_{MAX}$	Tested Limit (2)			9.9				
Sensor gain	· WIIN - · A - · WAX	Design Limit ⁽³⁾			0.0			9.9	
(average slope)	-40°C ≤ T _J ≤ 125°C			10			10	0.0	mV/°C
		Tested Limit (2)			10.1				
	10 0 = 11 = 120 0	Design Limit ⁽³⁾			10.1			10.1	
		Doolgii Liiiik		±0.4			±0.4	10.1	
	T _A = 25°C	Tested Limit (2)		20.1	±1		20.1	±1	
Load regulation (5)	. A 20 0	Design Limit ⁽³⁾							
$0 \le I_L \le 1 \text{ mA}$		Doolgii Liiiik		±0.5			±0.5		mV/mA
_	$T_{MIN} \le T_A \le T_{MAX}$	Tested Limit (2)		20.0			20.0		
	$-40^{\circ}\text{C} \le \text{T}_{\text{J}} \le 125^{\circ}\text{C}$	Design Limit ⁽³⁾			±3			±3	
		Doorger Emilie		±0.01			±0.01	<u>-</u> 0	
	T _A = 25°C	Tested Limit (2)		±0.01	±0.05		±0.01	±0.05	
	1A - 25 0	Design Limit ⁽³⁾			±0.00			±0.00	
Line regulation (5)		Dosign Limit		±0.02			±0.02		mV/V
	4 V ≤ V _S ≤ 30 V,	Tested Limit ⁽²⁾		±0.02			±0.02		
	-40°C ≤ T _J ≤ 125°C	Design Limit (3)			.0.1			.0.4	
		Design Limit			±0.1			±0.1	

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⁽¹⁾ Accuracy is defined as the error between the output voltage and 10 mv/°C times the case temperature of the device, at specified conditions of voltage, current, and temperature (expressed in °C).

⁽²⁾ Tested Limits are ensured and 100% tested in production.

⁽³⁾ Design Limits are ensured (but not 100% production tested) over the indicated temperature and supply voltage ranges. These limits are not used to calculate outgoing quality levels.

⁽⁴⁾ Non-linearity is defined as the deviation of the output-voltage-versus-temperature curve from the best-fit straight line, over the rated temperature range of the device.

⁽⁵⁾ Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output due to heating effects can be computed by multiplying the internal dissipation by the thermal resistance.



Electrical Characteristics: LM35A, LM35CA (continued)

Unless otherwise noted, these specifications apply: $-55^{\circ}C \le T_{J} \le 150^{\circ}C$ for the LM35 and LM35A; $-40^{\circ}C \le T_{J} \le 110^{\circ}C$ for the LM35C and LM35CA; and $0^{\circ}C \le T_{J} \le 100^{\circ}C$ for the LM35D. $V_{S} = 5$ Vdc and $I_{LOAD} = 50$ μ A, in the circuit of Full-Range Centigrade Temperature Sensor. These specifications also apply from 2°C to T_{MAX} in the circuit of Figure 14.

PARAMETER	TEST COND		LM35A		L	M35CA		UNIT	
PARAMETER	TEST COND	IIIONS	MIN	TYP	MAX	TYP	TYP	MAX	UNII
				56			56		
	V _S = 5 V, 25°C	Tested Limit ⁽²⁾			67			67	
		Design Limit ⁽³⁾							
				105			91		
	$V_S = 5 V$, -40°C ≤ $T_J \le 125$ °C	Tested Limit ⁽²⁾							
Quiescent	40 0 = 1j = 120 0	Design Limit ⁽³⁾			131			114	
current ⁽⁶⁾				56.2			56.2		μA
	V _S = 30 V, 25°C	Tested Limit (2)			68			68	
		Design Limit ⁽³⁾							
				105.5			91.5		
$V_S = 30 \text{ V},$ $-40^{\circ}\text{C} \le \text{T}_J \le 125^{\circ}\text{C}$	V _S = 30 V, -40°C ≤ T₁ ≤ 125°C	Tested Limit (2)							
	10 0 = 1j = 120 0	Design Limit ⁽³⁾			133			116	
				0.2			0.2		
	4 V ≤ V _S ≤ 30 V, 25°C	Tested Limit (2)			1			1	
Change of		Design Limit ⁽³⁾							
quiescent current ⁽⁵⁾				0.5			0.5		μA
	4 V ≤ V _S ≤ 30 V, -40°C ≤ T _J ≤ 125°C	Tested Limit (2)							
	10 0 1 1j 1 120 0	Design Limit ⁽³⁾			2			2	
Temperature				0.39			0.39		
coefficient of	–40°C ≤ T _J ≤ 125°C	Tested Limit ⁽²⁾							μΑ/°C
quiescent current		Design Limit ⁽³⁾			0.5			0.5	
Minimum				1.5			1.5		
temperature for	In circuit of Figure 14, $I_L = 0$	Tested Limit (2)							°C
rate accuracy	Ŭ	Design Limit ⁽³⁾			2			2	
Long term stability	$T_J = T_{MAX}$, for 1000 hours			±0.08			±0.08		°C

⁽⁶⁾ Quiescent current is defined in the circuit of Figure 14.



6.7 Electrical Characteristics: LM35, LM35C, LM35D Limits

Unless otherwise noted, these specifications apply: $-55^{\circ}\text{C} \le T_{J} \le 150^{\circ}\text{C}$ for the LM35 and LM35A; $-40^{\circ}\text{C} \le T_{J} \le 110^{\circ}\text{C}$ for the LM35C and LM35CA; and $0^{\circ}\text{C} \le T_{J} \le 100^{\circ}\text{C}$ for the LM35D. $V_{S} = 5$ Vdc and $I_{LOAD} = 50$ μA , in the circuit of Full-Range Centigrade Temperature Sensor. These specifications also apply from 2°C to T_{MAX} in the circuit of Figure 14.

		LM35			L			
PARAMETER	TEST CONDITIONS	TYP	TESTED LIMIT ⁽¹⁾	DESIGN LIMIT ⁽²⁾	TYP	TESTED LIMIT ⁽¹⁾	DESIGN LIMIT ⁽²⁾	UNIT
	$T_A = 25$ °C	±0.4	±1		±0.4	±1		
Accuracy, LM35,	$T_A = -10$ °C	±0.5			±0.5		±1.5	°C
LM35C ⁽³⁾	$T_A = T_{MAX}$	±0.8	±1.5		±0.8		±1.5	C
	$T_A = T_{MIN}$	±0.8		±1.5	±0.8		±2	
	$T_A = 25$ °C				±0.6	±1.5		
Accuracy, LM35D ⁽³⁾	$T_A = T_{MAX}$				±0.9		±2	°C
	$T_A = T_{MIN}$				±0.9		±2	
Nonlinearity (4)	$\begin{split} T_{\text{MIN}} &\leq T_{\text{A}} \leq T_{\text{MAX}}, \\ -40^{\circ}\text{C} &\leq T_{\text{J}} \leq 125^{\circ}\text{C} \end{split}$	±0.3		±0.5	±0.2		±0.5	°C
Sensor gain	$T_{MIN} \le T_A \le T_{MAX},$ -40°C \le T_J \le 125°C	10	9.8		10		9.8	mV/°C
(average slope)		10	10.2		10		10.2	
Load regulation (5)	T _A = 25°C	±0.4	±2		±0.4	±2		mV/mA
$0 \le I_L \le 1 \text{ mA}$	$T_{MIN} \le T_A \le T_{MAX},$ -40°C \le T_J \le 125°C	±0.5		±5	±0.5		±5	
	T _A = 25°C	±0.01	±0.1		±0.01	±0.1		
Line regulation (5)	4 V ≤ V _S ≤ 30 V, -40°C ≤ T _J ≤ 125°C	±0.02		±0.2	±0.02		±0.2	mV/V
	V _S = 5 V, 25°C	56	80		56	80		
Quiescent current ⁽⁶⁾	$V_S = 5 \text{ V}, -40^{\circ}\text{C} \le T_J \le 125^{\circ}\text{C}$	105		158	91		138	μA
Quiescent current	V _S = 30 V, 25°C	56.2	82		56.2	82		μΑ
	$V_S = 30 \text{ V}, -40^{\circ}\text{C} \le T_J \le 125^{\circ}\text{C}$	105.5		161	91.5		141	
Change of quiescent	4 V ≤ V _S ≤ 30 V, 25°C	0.2	2		0.2	2		
current ⁽⁵⁾	4 V ≤ V _S ≤ 30 V, -40°C ≤ T _J ≤ 125°C	0.5		3	0.5		3	μA
Temperature coefficient of quiescent current	-40°C ≤ T _J ≤ 125°C	0.39		0.7	0.39		0.7	μΑ/°C
Minimum temperature for rate accuracy	In circuit of Figure 14, I _L = 0	1.5		2	1.5		2	°C
Long term stability	$T_J = T_{MAX}$, for 1000 hours	±0.08			±0.08			°C

- (1) Tested Limits are ensured and 100% tested in production.
- (2) Design Limits are ensured (but not 100% production tested) over the indicated temperature and supply voltage ranges. These limits are not used to calculate outgoing quality levels.
- (3) Accuracy is defined as the error between the output voltage and 10 mv/°C times the case temperature of the device, at specified conditions of voltage, current, and temperature (expressed in °C).
- (4) Non-linearity is defined as the deviation of the output-voltage-versus-temperature curve from the best-fit straight line, over the rated temperature range of the device.
- (5) Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output due to heating effects can be computed by multiplying the internal dissipation by the thermal resistance.
- (6) Quiescent current is defined in the circuit of Figure 14.



6.8 Electrical Characteristics: LM35, LM35C, LM35D

Unless otherwise noted, these specifications apply: $-55^{\circ}\text{C} \le \text{T}_{\text{J}} \le 150^{\circ}\text{C}$ for the LM35 and LM35A; $-40^{\circ}\text{C} \le \text{T}_{\text{J}} \le 110^{\circ}\text{C}$ for the LM35C and LM35CA; and $0^{\circ}\text{C} \le \text{T}_{\text{J}} \le 100^{\circ}\text{C}$ for the LM35D. $V_S = 5$ Vdc and $I_{\text{LOAD}} = 50~\mu\text{A}$, in the circuit of Full-Range Centigrade Temperature Sensor. These specifications also apply from 2°C to T_{MAX} in the circuit of Figure 14.

DADAMETER	TEST OF		LM35		LM3	5C, LM35	D	LIAUT				
PARAMETER	TEST CO	NDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNIT			
				±0.4			±0.4					
	T _A = 25°C	Tested Limit ⁽²⁾			±1			±1				
		Design Limit (3)										
				±0.5			±0.5					
	$T_A = -10^{\circ}C$	Tested Limit ⁽²⁾										
Accuracy, LM35,		Design Limit ⁽³⁾						±1.5	°C			
LM35C ⁽¹⁾				±0.8			±0.8		O			
	$T_A = T_{MAX}$	Tested Limit ⁽²⁾			±1.5							
		Design Limit ⁽³⁾						±1.5				
				±0.8			±0.8					
	$T_A = T_{MIN}$	Tested Limit ⁽²⁾										
		Design Limit ⁽³⁾			±1.5			±2				
Accuracy, LM35D ⁽¹⁾							±0.6	6				
	T _A = 25°C	Tested Limit ⁽²⁾						±1.5				
		Design Limit ⁽³⁾							°C			
							±0.9					
	$T_A = T_{MAX}$	Tested Limit ⁽²⁾										
LIVIOOD		Design Limit ⁽³⁾						±2				
	$T_{A} = T_{MIN}$ $T_{MIN} \le T_{A} \le T_{MAX},$ $-40^{\circ}C \le T_{J} \le 125^{\circ}C$					·	±0.9					
		Tested Limit ⁽²⁾				·						
		Design Limit ⁽³⁾				·		±2				
				±0.3		·	±0.2					
Nonlinearity (4)		Tested Limit ⁽²⁾							°C			
	40 0 2 1j 2 120 0	Design Limit ⁽³⁾			±0.5	·		±0.5				
				10			10					
	$T_{MIN} \le T_A \le T_{MAX}$, -40°C \le T_J \le 125°C	Tested Limit (2)			9.8	·						
Sensor gain	-40 0 ± 1j ± 125 0	Design Limit ⁽³⁾				·		9.8	\//00			
(average slope)				10			10		mV/°C			
		Tested Limit ⁽²⁾			10.2							
		Design Limit ⁽³⁾						10.2				
				±0.4			±0.4					
	T _A = 25°C	Tested Limit ⁽²⁾			±2			±2	mV/mA			
Load regulation (5)		Design Limit ⁽³⁾										
0 ≤ I _L ≤ 1 mA				±0.5			±0.5					
	$T_{MIN} \le T_A \le T_{MAX},$ -40°C \le T_J \le 125°C	Tested Limit ⁽²⁾										
	-40°C ≤ 1 _J ≤ 125°C	Design Limit ⁽³⁾			±5			±5				

⁽¹⁾ Accuracy is defined as the error between the output voltage and 10 mv/°C times the case temperature of the device, at specified conditions of voltage, current, and temperature (expressed in °C).

⁽²⁾ Tested Limits are ensured and 100% tested in production.

⁽³⁾ Design Limits are ensured (but not 100% production tested) over the indicated temperature and supply voltage ranges. These limits are not used to calculate outgoing quality levels.

⁽⁴⁾ Non-linearity is defined as the deviation of the output-voltage-versus-temperature curve from the best-fit straight line, over the rated temperature range of the device.

⁽⁵⁾ Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output due to heating effects can be computed by multiplying the internal dissipation by the thermal resistance.



Electrical Characteristics: LM35, LM35C, LM35D (continued)

Unless otherwise noted, these specifications apply: $-55^{\circ}C \le T_{J} \le 150^{\circ}C$ for the LM35 and LM35A; $-40^{\circ}C \le T_{J} \le 110^{\circ}C$ for the LM35C and LM35CA; and 0°C \leq T_J \leq 100°C for the LM35D. V_S = 5 Vdc and I_{LOAD} = 50 μ A, in the circuit of Full-Range Centigrade Temperature Sensor. These specifications also apply from 2°C to T_{MAX} in the circuit of Figure 14.

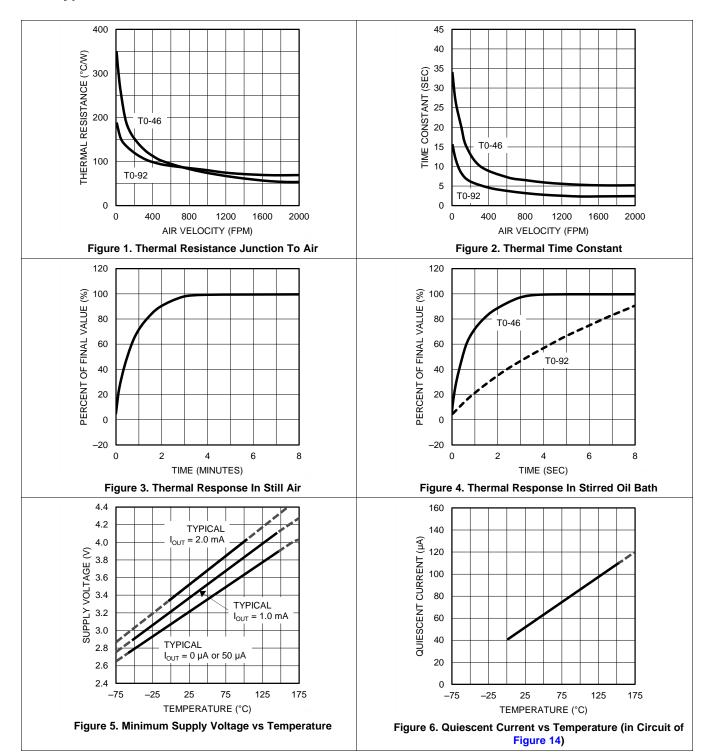
DARAMETER	TEST CONDI	TIONS		LM35		LM3	5C, LM35	D	UNIT	
PARAMETER	TEST CONDI	MIN	TYP	MAX	MIN	TYP	MAX	UNII		
				±0.01			±0.01			
(5)	T _A = 25°C	Tested Limit ⁽²⁾			±0.1	·				
		Design Limit ⁽³⁾				·		±0.1		
Line regulation (5)				±0.02		·	±0.02		mV/V	
	4 V ≤ V _S ≤ 30 V, -40°C ≤ T _J ≤ 125°C	Tested Limit (2)								
	-40 C = 1] = 125 C	Design Limit ⁽³⁾			±0.2			±0.2		
				56		·	56			
	V _S = 5 V, 25°C	Tested Limit ⁽²⁾			80			80		
		Design Limit ⁽³⁾				·				
				105		·	91			
	$V_S = 5 \text{ V}, -40^{\circ}\text{C} \le T_J \le 125^{\circ}\text{C}$	Tested Limit ⁽²⁾				·			μА	
Quiescent current ⁽⁶⁾		Design Limit ⁽³⁾			158	·		138		
	V _S = 30 V, 25°C			56.2		·	56.2			
		Tested Limit ⁽²⁾			82	·		82		
		Design Limit ⁽³⁾				·				
	V _S = 30 V, -40°C ≤ T _J ≤ 125°C			105.5		·	91.5			
		Tested Limit ⁽²⁾				·				
		Design Limit ⁽³⁾			161	·		141		
				0.2		·	0.2			
	4 V ≤ V _S ≤ 30 V, 25°C	Tested Limit ⁽²⁾				·		2		
Change of		Design Limit ⁽³⁾			2	·				
quiescent current ⁽⁵⁾				0.5		·	0.5		μΑ	
	4 V ≤ V _S ≤ 30 V, -40°C ≤ T _J ≤ 125°C	Tested Limit ⁽²⁾				·				
	-40 0 ± 1j ± 125 0	Design Limit ⁽³⁾			3	·		3		
Temperature				0.39		·	0.39			
coefficient of quiescent current	-40°C ≤ T _J ≤ 125°C	Tested Limit ⁽²⁾				·			μΑ/°C	
		Design Limit ⁽³⁾			0.7			0.7		
Minimum				1.5			1.5			
temperature for	In circuit of Figure 14, $I_L = 0$	Tested Limit ⁽²⁾							°C	
rate accuracy		Design Limit ⁽³⁾			2			2		
Long term stability	$T_J = T_{MAX}$, for 1000 hours			±0.08		·	±0.08		°C	

⁽⁶⁾ Quiescent current is defined in the circuit of Figure 14.

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6.9 Typical Characteristics



TEXAS INSTRUMENTS

Typical Characteristics (continued)

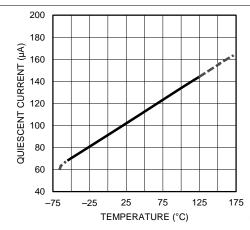


Figure 7. Quiescent Current vs Temperature (in Circuit of Full-Range Centigrade Temperature Sensor)

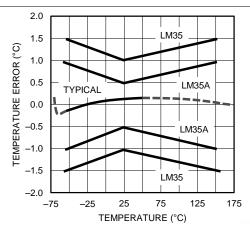


Figure 8. Accuracy vs Temperature (Ensured)

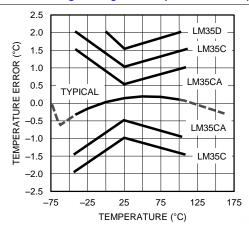


Figure 9. Accuracy vs Temperature (Ensured)

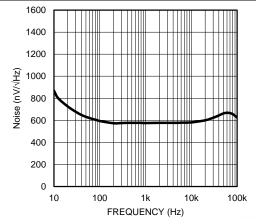


Figure 10. Noise Voltage

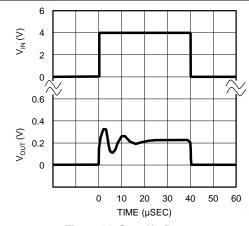


Figure 11. Start-Up Response



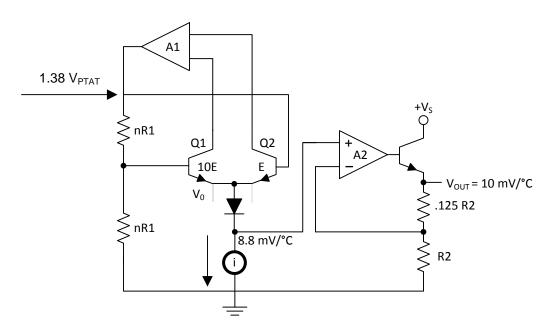
7 Detailed Description

7.1 Overview

The LM35-series devices are precision integrated-circuit temperature sensors, with an output voltage linearly proportional to the Centigrade temperature. The LM35 device has an advantage over linear temperature sensors calibrated in Kelvin, as the user is not required to subtract a large constant voltage from the output to obtain convenient Centigrade scaling. The LM35 device does not require any external calibration or trimming to provide typical accuracies of \pm ½ °C at room temperature and \pm ¾ °C over a full -55°C to 150°C temperature range. Lower cost is assured by trimming and calibration at the wafer level. The low output impedance, linear output, and precise inherent calibration of the LM35 device makes interfacing to readout or control circuitry especially easy. The device is used with single power supplies, or with plus and minus supplies. As the LM35 device draws only 60 μ A from the supply, it has very low self-heating of less than 0.1°C in still air. The LM35 device is rated to operate over a -55°C to 150°C temperature range, while the LM35C device is rated for a -40°C to 110°C range (-10° with improved accuracy). The temperature-sensing element is comprised of a delta-V BE architecture.

The temperature-sensing element is then buffered by an amplifier and provided to the VOUT pin. The amplifier has a simple class A output stage with typical $0.5-\Omega$ output impedance as shown in the *Functional Block Diagram*. Therefore the LM35 can only source current and it's sinking capability is limited to 1 μ A.

7.2 Functional Block Diagram



7.3 Feature Description

7.3.1 LM35 Transfer Function

The accuracy specifications of the LM35 are given with respect to a simple linear transfer function:

$$V_{OUT} = 10 \text{ mv/}^{\circ}\text{C} \times \text{T}$$

where

V_{OUT} is the LM35 output voltage

7.4 Device Functional Modes

The only functional mode of the LM35 is that it has an analog output directly proportional to temperature.



8 Application and Implementation

NOTE

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

8.1 Application Information

The features of the LM35 make it suitable for many general temperature sensing applications. Multiple package options expand on it's flexibility.

8.1.1 Capacitive Drive Capability

Like most micropower circuits, the LM35 device has a limited ability to drive heavy capacitive loads. Alone, the LM35 device is able to drive 50 pF without special precautions. If heavier loads are anticipated, isolating or decoupling the load with a resistor is easy (see Figure 12). The tolerance of capacitance can be improved with a series R-C damper from output to ground (see Figure 13).

When the LM35 device is applied with a $200-\Omega$ load resistor as shown in Figure 16, Figure 17, or Figure 19, the device is relatively immune to wiring capacitance because the capacitance forms a bypass from ground to input and not on the output. However, as with any linear circuit connected to wires in a hostile environment, performance is affected adversely by intense electromagnetic sources (such as relays, radio transmitters, motors with arcing brushes, and SCR transients), because the wiring acts as a receiving antenna and the internal junctions act as rectifiers. For best results in such cases, a bypass capacitor from V_{IN} to ground and a series R-C damper, such as 75 Ω in series with 0.2 or 1 μ F from output to ground, are often useful. Examples are shown in Figure 13, Figure 24, and Figure 25.

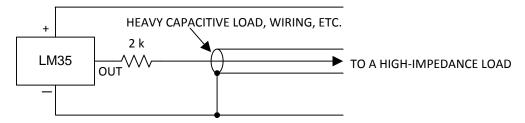


Figure 12. LM35 with Decoupling from Capacitive Load

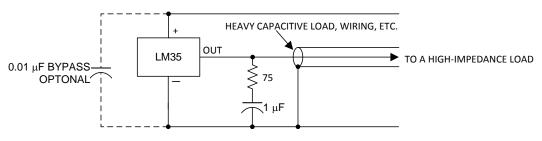


Figure 13. LM35 with R-C Damper



8.2 Typical Application

8.2.1 Basic Centigrade Temperature Sensor

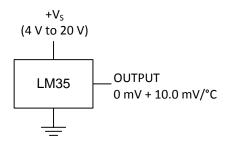


Figure 14. Basic Centigrade Temperature Sensor (2 °C to 150 °C)

8.2.1.1 Design Requirements

Table 1. Design Parameters

PARAMETER	VALUE
Accuracy at 25°C	±0.5°C
Accuracy from -55 °C to 150°C	±1°C
Temperature Slope	10 mV/°C

8.2.1.2 Detailed Design Procedure

Because the LM35 device is a simple temperature sensor that provides an analog output, design requirements related to layout are more important than electrical requirements. For a detailed description, refer to the *Layout*.

8.2.1.3 Application Curve

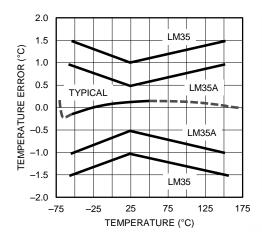


Figure 15. Accuracy vs Temperature (Ensured)

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8.3 System Examples

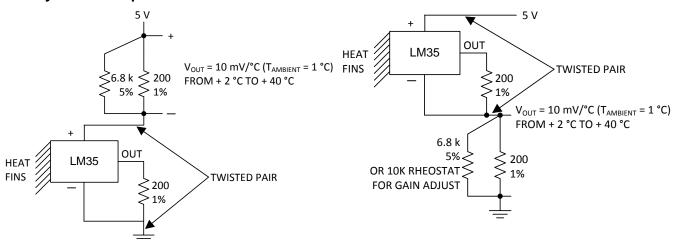


Figure 16. Two-Wire Remote Temperature Sensor (Grounded Sensor)

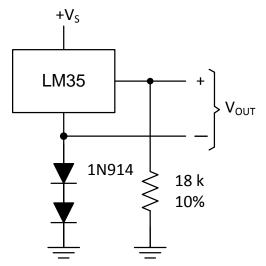


Figure 18. Temperature Sensor, Single Supply (-55° to +150°C)

Figure 17. Two-Wire Remote Temperature Sensor (Output Referred to Ground)

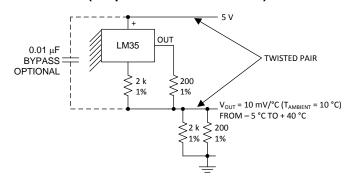
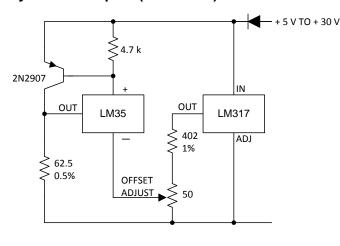


Figure 19. Two-Wire Remote Temperature Sensor (Output Referred to Ground)



System Examples (continued)



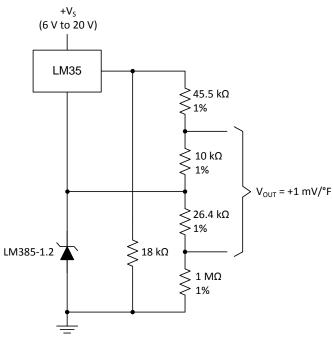


Figure 20. 4-To-20 mA Current Source (0°C to 100°C)

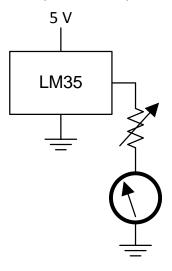


Figure 22. Centigrade Thermometer (Analog Meter)

Figure 21. Fahrenheit Thermometer

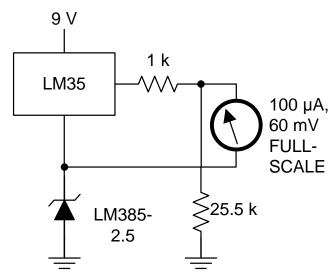


Figure 23. Fahrenheit Thermometer, Expanded Scale Thermometer (50°F to 80°F, for Example Shown)



System Examples (continued)

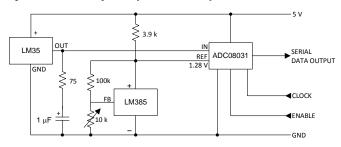


Figure 24. Temperature to Digital Converter (Serial Output) (128°C Full Scale)

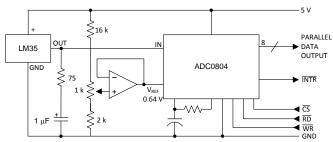
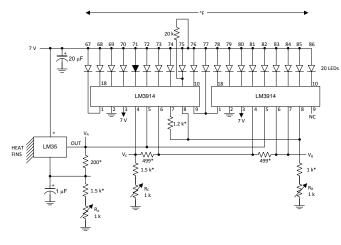
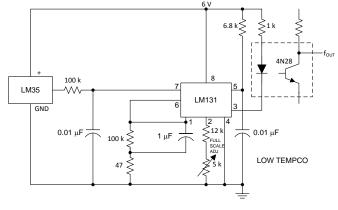


Figure 25. Temperature to Digital Converter (Parallel TRI-STATE Outputs for Standard Data Bus to μP Interface) (128°C Full Scale)





*=1% or 2% film resistor

Trim R_B for $V_B = 3.075 \text{ V}$

Trim R_C for $V_C = 1.955 V$

Trim R_A for $V_A = 0.075 \text{ V} + 100 \text{ mV/°C } \times T_{ambient}$

Example, $V_A = 2.275 \text{ V}$ at 22°C

Figure 26. Bar-Graph Temperature Display (Dot Mode)

Figure 27. LM35 With Voltage-To-Frequency Converter and Isolated Output (2°C to 150°C; 20 to 1500 Hz)



9 Power Supply Recommendations

The LM35 device has a very wide 4-V to 30-V power supply voltage range, which makes it ideal for many applications. In noisy environments, TI recommends adding a 0.1 μ F from V+ to GND to bypass the power supply voltage. Larger capacitances maybe required and are dependent on the power-supply noise.

10 Layout

10.1 Layout Guidelines

The LM35 is easily applied in the same way as other integrated-circuit temperature sensors. Glue or cement the device to a surface and the temperature should be within about 0.01°C of the surface temperature.

The 0.01°C proximity presumes that the ambient air temperature is almost the same as the surface temperature. If the air temperature were much higher or lower than the surface temperature, the actual temperature of the LM35 die would be at an intermediate temperature between the surface temperature and the air temperature; this is especially true for the TO-92 plastic package. The copper leads in the TO-92 package are the principal thermal path to carry heat into the device, so its temperature might be closer to the air temperature than to the surface temperature.

Ensure that the wiring leaving the LM35 device is held at the same temperature as the surface of interest to minimize the temperature problem. The easiest fix is to cover up these wires with a bead of epoxy. The epoxy bead will ensure that the leads and wires are all at the same temperature as the surface, and that the temperature of the LM35 die is not affected by the air temperature.

The TO-46 metal package can also be soldered to a metal surface or pipe without damage. Of course, in that case the V- terminal of the circuit will be grounded to that metal. Alternatively, mount the LM35 inside a sealed-end metal tube, and then dip into a bath or screw into a threaded hole in a tank. As with any IC, the LM35 device and accompanying wiring and circuits must be kept insulated and dry, to avoid leakage and corrosion. This is especially true if the circuit may operate at cold temperatures where condensation can occur. Printed-circuit coatings and varnishes such as a conformal coating and epoxy paints or dips are often used to insure that moisture cannot corrode the LM35 device or its connections.

These devices are sometimes soldered to a small light-weight heat fin to decrease the thermal time constant and speed up the response in slowly-moving air. On the other hand, a small thermal mass may be added to the sensor, to give the steadiest reading despite small deviations in the air temperature.

_				, 6		,,	JA/
	TO, no heat sink	TO ⁽¹⁾ , small heat fin	TO-92, no heat sink	TO-92 ⁽²⁾ , small heat fin	SOIC-8, no heat sink	SOIC-8 ⁽²⁾ , small heat fin	TO-220, no heat sink
Still air	400°C/W	100°C/W	180°C/W	140°C/W	220°C/W	110°C/W	90°C/W
Moving air	100°C/W	40°C/W	90°C/W	70°C/W	105°C/W	90°C/W	26°C/W
Still oil	100°C/W	40°C/W	90°C/W	70°C/W	_	_	_
Stirred oil	50°C/W	30°C/W	45°C/W	40°C/W	_	_	_
(Clamped to metal, Infinite heat sink)	(24°	C/W)	_	_	(55°0	C/W)	_

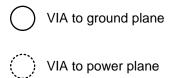
Table 2. Temperature Rise of LM35 Due To Self-heating (Thermal Resistance, Raja)

⁽¹⁾ Wakefield type 201, or 1-in disc of 0.02-in sheet brass, soldered to case, or similar.

⁽²⁾ TO-92 and SOIC-8 packages glued and leads soldered to 1-in square of 1/16-in printed circuit board with 2-oz foil or similar.



10.2 Layout Example



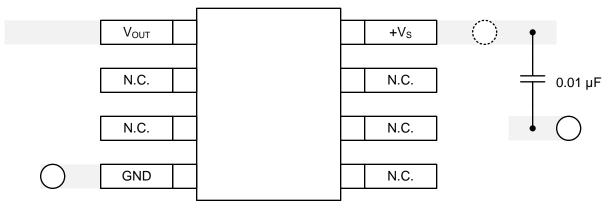


Figure 28. Layout Example



11 Device and Documentation Support

11.0.1 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on ti.com. In the upper right corner, click on A*lert me* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document

11.0.2 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

TI E2E™ Online Community TI's Engineer-to-Engineer (E2E) Community. Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

Design Support *TI's Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

11.1 Trademarks

E2E is a trademark of Texas Instruments.
All other trademarks are the property of their respective owners.

11.2 Electrostatic Discharge Caution



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

11.3 Glossary

SLYZ022 — TI Glossary.

This glossary lists and explains terms, acronyms, and definitions.

12 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

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15-Aug-2016

PACKAGING INFORMATION

Orderable Device	Status	Package Type	_	Pins		Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Sample
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
LM35AH	ACTIVE	ТО	NDV	3	500	TBD	Call TI	Call TI	-55 to 150	(LM35AH ~ LM35AH)	Sample
LM35AH/NOPB	ACTIVE	ТО	NDV	3	500	Green (RoHS & no Sb/Br)	Call TI	Level-1-NA-UNLIM	-55 to 150	(LM35AH ~ LM35AH)	Sample
LM35CAH	ACTIVE	ТО	NDV	3	500	TBD	Call TI	Call TI	-40 to 110	(LM35CAH ~ LM35CAH)	Sample
LM35CAH/NOPB	ACTIVE	ТО	NDV	3	500	Green (RoHS & no Sb/Br)	Call TI	Level-1-NA-UNLIM	-40 to 110	(LM35CAH ~ LM35CAH)	Sample
LM35CAZ/LFT4	ACTIVE	TO-92	LP	3	2000	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type		LM35 CAZ	Sample
LM35CAZ/NOPB	ACTIVE	TO-92	LP	3	1800	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	-40 to 110	LM35 CAZ	Sample
LM35CH	ACTIVE	ТО	NDV	3	500	TBD	Call TI	Call TI	-40 to 110	(LM35CH ~ LM35CH)	Sample
LM35CH/NOPB	ACTIVE	ТО	NDV	3	500	Green (RoHS & no Sb/Br)	Call TI	Level-1-NA-UNLIM	-40 to 110	(LM35CH ~ LM35CH)	Sample
LM35CZ/LFT1	ACTIVE	TO-92	LP	3	2000	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type		LM35 CZ	Sampl
LM35CZ/NOPB	ACTIVE	TO-92	LP	3	1800	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	-40 to 110	LM35 CZ	Sampl
LM35DH	ACTIVE	ТО	NDV	3	1000	TBD	Call TI	Call TI	0 to 70	(LM35DH ~ LM35DH)	Sampl
LM35DH/NOPB	ACTIVE	ТО	NDV	3	1000	Green (RoHS & no Sb/Br)	Call TI POST-PLATE	Level-1-NA-UNLIM	0 to 70	(LM35DH ~ LM35DH)	Sampl
LM35DM	NRND	SOIC	D	8	95	TBD	Call TI	Call TI	0 to 100	LM35D M	
LM35DM/NOPB	ACTIVE	SOIC	D	8	95	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	0 to 100	LM35D M	Sampl
LM35DMX	NRND	SOIC	D	8	2500	TBD	Call TI	Call TI	0 to 100	LM35D M	
LM35DMX/NOPB	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	0 to 100	LM35D M	Sampl
LM35DT	NRND	TO-220	NEB	3	45	TBD	Call TI	Call TI	0 to 100	LM35DT	
LM35DT/NOPB	ACTIVE	TO-220	NEB	3	45	Green (RoHS & no Sb/Br)	CU SN	Level-1-NA-UNLIM	0 to 100	LM35DT	Samp





15-Aug-2016

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
LM35DZ	OBSOLETE	TO-92	LP	3		TBD	(6) Call TI	Call TI		(4/5)	
LM35DZ/LFT1	ACTIVE	TO-92	LP	3	2000	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type		LM35 DZ	Samples
LM35DZ/LFT4	ACTIVE	TO-92	LP	3	2000	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type		LM35 DZ	Samples
LM35DZ/NOPB	ACTIVE	TO-92	LP	3	1800	Green (RoHS & no Sb/Br)	CU SN	N / A for Pkg Type	0 to 100	LM35 DZ	Samples
LM35H	ACTIVE	ТО	NDV	3	500	TBD	Call TI	Call TI	-55 to 150	(LM35H ~ LM35H)	Samples
LM35H/NOPB	ACTIVE	ТО	NDV	3	500	Green (RoHS & no Sb/Br)	Call TI	Level-1-NA-UNLIM	-55 to 150	(LM35H ~ LM35H)	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.



PACKAGE OPTION ADDENDUM

15-Aug-2016

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PACKAGE MATERIALS INFORMATION

www.ti.com 15-Aug-2016

TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

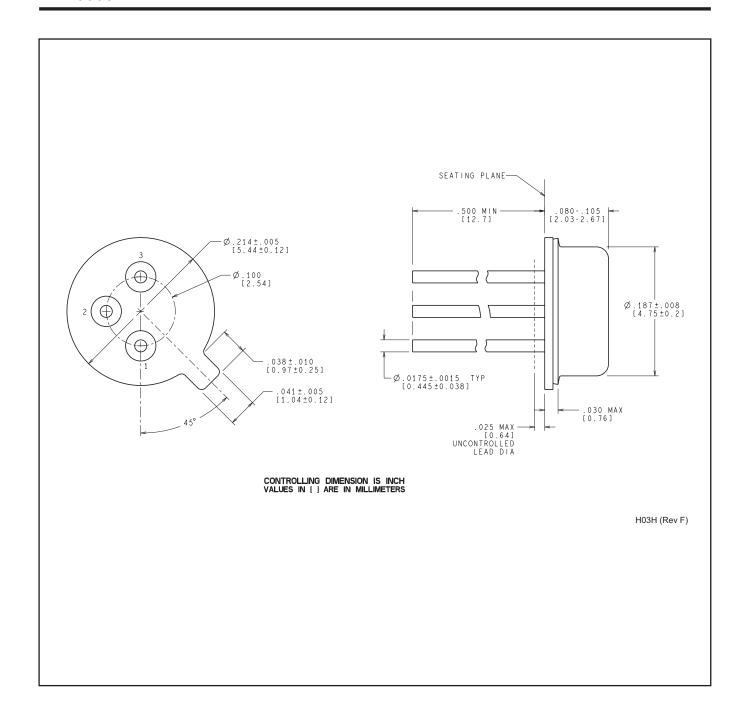
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM35DMX	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1
LM35DMX/NOPB	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1

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*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM35DMX	SOIC	D	8	2500	367.0	367.0	35.0
LM35DMX/NOPB	SOIC	D	8	2500	367.0	367.0	35.0



D (R-PDSO-G8)

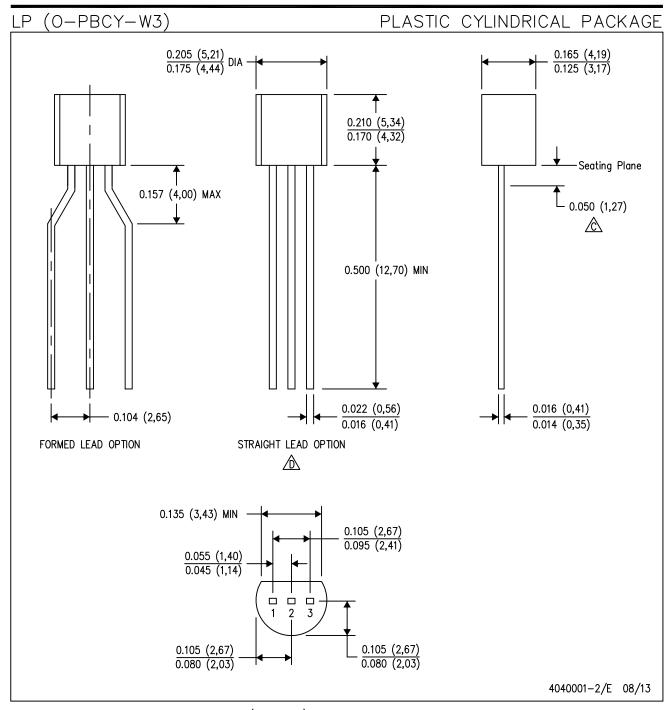
PLASTIC SMALL OUTLINE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AA.





NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

Lead dimensions are not controlled within this area.

Falls within JEDEC TO−226 Variation AA (TO−226 replaces TO−92).

E. Shipping Method:

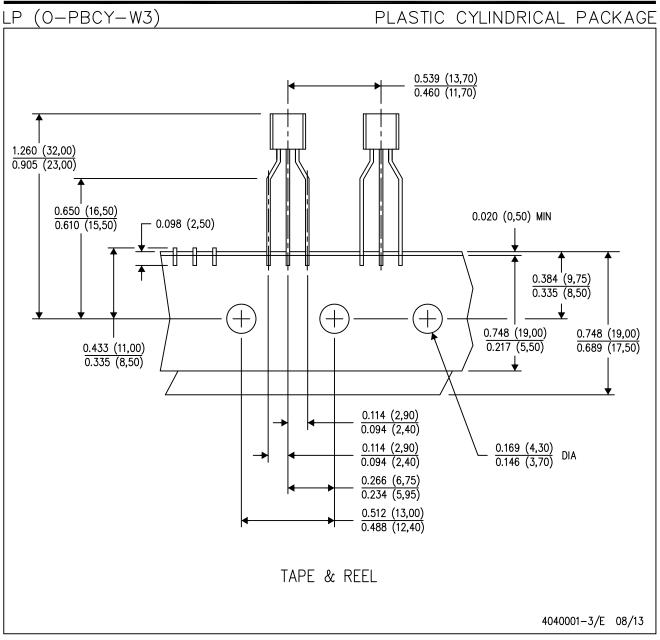
Straight lead option available in bulk pack only.

Formed lead option available in tape & reel or ammo pack.

Specific products can be offered in limited combinations of shipping mediums and lead options.

Consult product folder for more information on available options.





NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Tape and Reel information for the Formed Lead Option package.

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